



MAR 22 2004

JC185

Information Disclosure Statement Transmittal

Docket No.: TRAN-P034

#29 2189
PatentDocket
3-29-04

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner of Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit.

Date of Deposit:	03/18/04	Name of Person Making the Deposit:	KATHERINE RINALDI	Signature of the Person Making the Deposit:	Katherine Rinaldi
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Sameer Halepete, H. Peter Anvin, Zong Chen, Godfrey P. D'Souza, Marc Fleishmann, Keith Klayman, Thomas Lawrence and Andrew Read

Serial No.: 09/484,516

Examiner: Myers, Paul R.

Filed: 01/18/00

Art Unit: 2189

For: ADAPTIVE POWER CONTROL

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Technology Center 2100

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Sir:

Information Disclosure Statement Transmittal

Transmitted herewith is the following:

Formal drawings, totaling sheets.
Informal drawings, totaling sheets.
Certification for PTO Consideration
Information Disclosure statement (sheets)
 Information Disclosure statement and late filing fee
 Form 1449
Petition for Extension of Time
 Other: REFERENCES

Fee Calculation (for other than a small entity)

Fee Items				Fee Rate	Total
Petition for Extension of Time (fee calculated elsewhere)				\$.00	\$0.00
Information Disclosure Statement, late filing				\$180.00	\$180.00
Other:					\$0.00
Total Fees					\$180.00

PAYMENT OF FEES

- The full fee due in connection with this communication is provided as follows:

[X] The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085. A duplicate copy of this authorization is enclosed.

[X] A check in the amount of \$180.00

[] Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date: 3/18/04

By: Ronald M Pomerenke
Ronald M. Pomerenke
Reg. No. 43,009



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: TRAN-P034

Inventor(s): Halepote, et al.

Group Art Unit:

Filed: 01/18/00

Examiner:

Serial No.: 09/484,516

Title: ADAPTIVE POWER CONTROL

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Commissioner of Patents
P. O. Box 1450
Alexandria, VA 22313-1450
Sir:

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Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

Pat. No.	Pat. Title	Grant Date
5,201,059	METHOD FOR REDUCING POWER CONSUMPTION INCLUDES COMPARING VARIANCE IN NUMBER OF TIME MICROPROCESSOR TRIED TO REACT INPUT IN PREDEFINED PERIOD TO PREDEFINED VARIANCE	04/06/93
5,230,055	BATTERY OPERATED COMPUTER OPERATION SUSPENSION IN RESPONSE TO ENVIRONMENTAL SENSOR INPUTS	07/20/93
5,752,011	METHOD AND SYSTEM FOR CONTROLLING A PROCESSOR'S CLOCK FREQUENCY IN ACCORDANCE WITH THE PROCESSOR'S TEMPERATURE	05/12/98
6,216,235	THERMAL AND POWER MANAGEMENT FOR COMPUTER SYSTEMS	04/10/01
5,167,024	POWER MANAGEMENT FOR A LAPTOP COMPUTER WITH SLOW AND SLEEP MODES	11/24/92
5,218,704	REAL-TIME POWER CONSERVATION FOR PORTABLE COMPUTERS	06/08/93
5,239,652	ARRANGMENT FOR REDUCING COMPUTER POWER CONSUMPTION BY TURNING OFF THE MICROPROCESSOR WHEN INACTIVE	08/24/93
5,682,093	APPARATUS AND METHOD FOR REDUCING THE POWER CONSUMPTION OF AN ELECTRONIC DEVICE	10/28/97
5,717,319	METHOD TO REDUCE THE POWER CONSUMPTION OF AN ELECTRONIC DEVICE COMPRISING A VOLTAGE REGULATOR	02/10/98
5,086,501	COMPUTING SYSTEM WITH SELECTIVE OPERATING VOLTAGE AND BUS SPEED	02/04/92
6,157,092	METHOD AND CIRCUIT CONFIGURATION FOR VOLTAGE SUPPLY IN ELECTRIC FUNCTION UNITS	12/05/00
6,118,306	CHANGING CLOCK FREQUENCY	09/12/00
5,592,173	GPS RECEIVER HAVING A LOW POWER STANDBY MODE	01/07/97
5,757,171	ON-BOARD VOLTAGE REGULATORS WITH AUTOMATIC PROCESSOR TYPE DETECTION	05/26/98
5,848,281	METHOD AND APPARATUS FOR POWDER MANAGEMENT IN A MULTI-FUNCTION CONTROLLER WITH AN EMBEDDED MICROPROCESSOR	12/08/98
5,923,545	METHOD AND APPARATUS FOR PROVIDING MULTIPLE OUTPUT VOLTAGES FROM A VOLTAGE REGULATOR	07/13/99

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The Examiner's attention is respectfully directed to the following Foreign Patent or Published Foreign Patents:

<u>Document No.</u>	<u>Title</u>	<u>Publication Date</u>
EPO474963	COMPUTER SYSTEM HAVING SLEEP MODE FUNCTION	03/18/92
EPO381021	POWER SAVING SYSTEM	08/08/90

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The Examiner's attention is respectfully directed to the following related documents:

"HIGH-SPEED, DIGITALLY ADUSTEDSTEPDOWN CONTROLLERS FOR NOTEBOOK CPUS"; Maxim Manual; pages 11 & 21

"OPERATIO U (Refer to Functional Diagram)"; LTC 1736; Linear Technology Manual; page 9

Please direct all correspondence concerning the above-identified application to the following address:

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